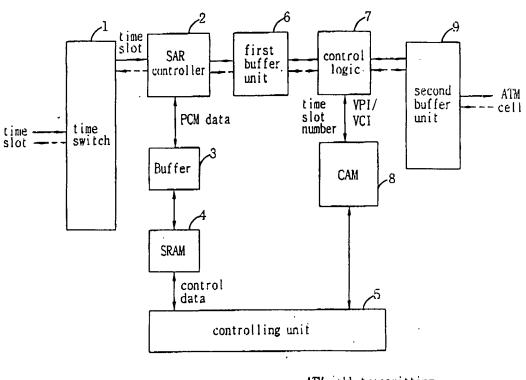
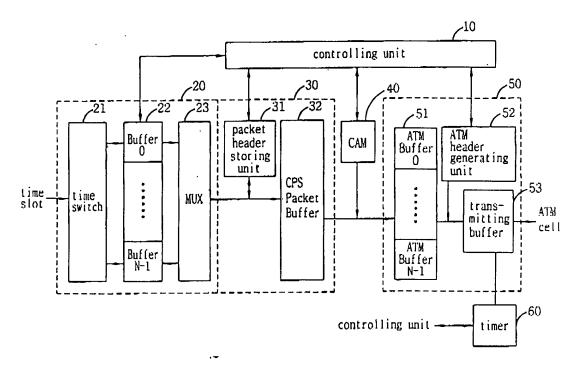
FIG. 1 BACKGROUND ART

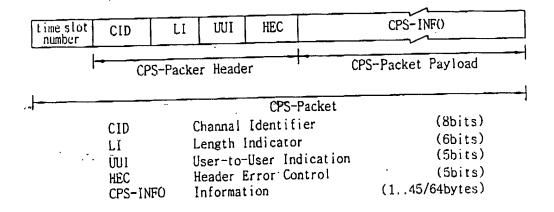


ATM cell transmitting
ATM cell receiving

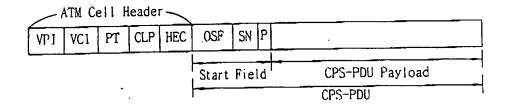
FIG. 2



## FIG. 3



## FIG. 4



VPT	Virtual path identifier	(12 bits)		Offset Field	•
VCI	Virtual channel identifier	(16 bits)	SN	Sequence Number	(1 bits)
	Payload type	(3 bits)	P	Parity	(1 bits)
	<del></del>	(1 bits)		•	
	Cell loss priority	(8 bits)			
HEC	Header error control	(0 0103/			

FIG. 5

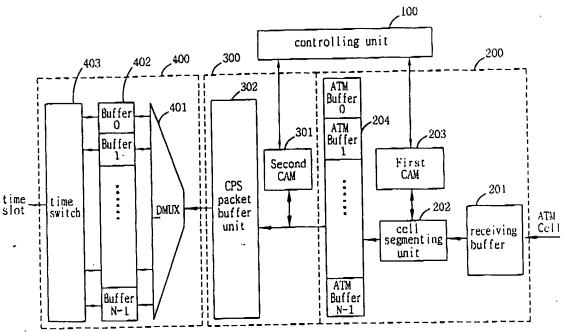


FIG. 6

CID	Ll	UUI	HEC	CPS-INFO		
- CP	S-Pack	et Head	ler	CPS-Packet Payload		
CPS-Packet						
LJ UUI		Channal Identifier Length Indicator User-to-User Indication Header Error Control Information		(8bits) (6bits) (5bits) (5bits) (145/64bytes)		